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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (Case No. P043D2C3C)

#

In the Application of:

FARMWALD, ET AL.

Serial No.: 09

09/492,982

Filed:

JANUARY 27, 2000

Title:

INTEGRATED CIRCUIT I/O USING A

HIGH PERFORMANCE INTERFACE

2824

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APR 0 3 2000

TECHNOLOGY CENTER 2800

Assistant Commissioner for Patents Washington, DC 20231

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that the attached 1) Transmittal of Information Disclosure

Statement (1 page and 1 copy thereof), and 2) Information Disclosure Statement

(6 pages and documents cited in IDS) is/are being deposited with the United States

Postal Service with sufficient postage as first class U.S. mail in an envelope addressed to:

Assistant Commissioner for Patents Washington, D.C. 20231

on February 28, 2000.

Michiko Sites

(Print Name of Person Signing Certificate)

IN THE UNI STATES PATENT AND TRADEMARK OFFICE (P0043D2C3C)

In re Application of:

FARMWALD ET AL.

Serial No: 09/492,982

Filed:

JANUARY 27, 2000

Title:

INTEGRATED CIRCUIT I/O USING A HIGH

PERFORMANCE INTERFACE

Commissioner of Patents and Trademarks Washington, D.C. 20231

Dear Sir:

With respect to the above-identified application, transmitted herewith is an **INFORMATION DISCLOSURE** STATEMENT.

Fees:	
Submission of Information Disclosure Statement under 37 CFR §1.97(b)	\$0
TOTAL FEE DUE:	\$0

- A check payable to the Commissioner of Patents and Trademarks, in the amount of \$ payment of the Total Fee Due.
- Please charge my Deposit Account No. 50-0998 in the amount of \$240.00 to cover the above fees. A duplicate copy of this sheet is enclosed.
- [XX] The Commissioner is hereby authorized to charge any additional fees which may be required, or credit any overpayment to Deposit Account No. 50-0998. A duplicate copy of this sheet is enclosed.

Date: February 28, 2000

Neil A. Steinberg

Respectfully submitted

Registration No. 34,735

Art Unit:

Examiner:

650-944-7772

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APR 0 3 2000

TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (Case No. P043D2C3C)

In the Application of:

FARMWALD et al.

Serial No: 09/492,982

JANUARY 27, 2000 Filed:

INTEGRATED CIRCUIT I/O USING A

HIGH PERFORMANCE BUS INTERFACE

Group

Art Unit:

Before

Examiner:

Assistant Commissioner for Patents Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In compliance with the duty of disclosure set forth in 37 C.F.R. §1.56, submitted herewith is a modified Form PTO-1449, including a copy of all of the documents cited therein -- except for those references which were provided or cited during the prosecution of App. S/N 09/252,997 to which priority is claimed (See 37 C.F.R. §1.98(d)).

It is believed that the Examiner may find the documents cited in the modified Form PTO-1449 material to the patentability of one or more of the claims in the above-captioned application. Accordingly, it is respectfully requested that the Examiner make his consideration of these references formally of record with the initial Office Action.

Date: February 28, 2000

Respectfully submitted,

Neil A. Steinberg, Esq.

Reg. No. 34,735

(650) 944-7772